Fig.1 (PRIOR ART)

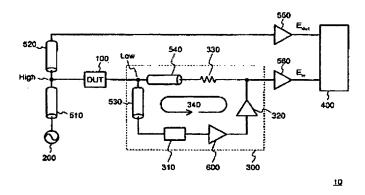


Fig. 2 (PRIOR ART)

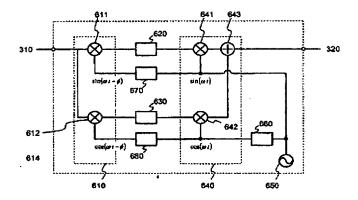


Fig. 3

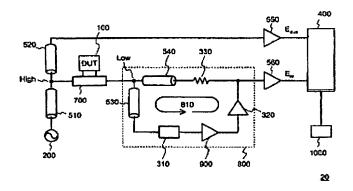


Fig. 4

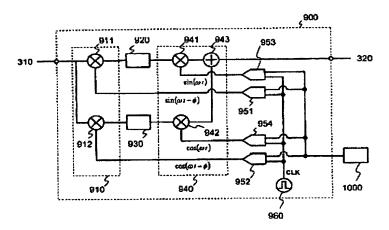
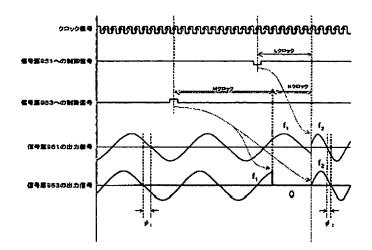


Fig. 5



[Key]
Clock signals
Control signals to signal source 951
Control signals to signal source 953
Output signals of signal source 951
Output signals of signal source 953

L clock M clock

N clock